

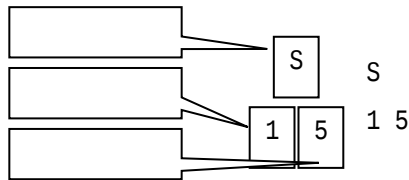


J1	J1-1	24V			150mA
	J1-2	24V			
	J1-3	CAN H			
	J1-4	CAN L			
J2	J2-1			0C	DC24V 20mA
	J2-2	24V			
	J2-3	0V			
	J2-4			光耦	8mA
J3	J3-1			0C	DC24V 20mA
	J3-2	24V			
	J3-3	0V			
	J3-4				8mA
J4	J4-1		[1]	0C	DC24V 20mA
	J4-2	24V			
	J4-3	0V			
	J4-4	[2]			8mA
J5	J5-1	A			DC5A24V
	J5-2	B			AC5A250V
	J5-3	A			DC5A24V
	J5-4	B			AC5A250V
J6	J6-1	A			DC5A24V
	J6-2	B			AC5A250V
	J6-3	24V			
	J6-4	0V			
S1	CAN				
P					
JC	[3]				
SZ	[3]				



SZ 2 “ S ” ,

SZ “ S ” ,



“ 1 ”
“ 0 ”

64

33

64

5.1

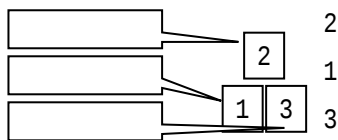
JC

2

5.2

U P U
3

P



1
3

5.3

6.21

“ ”

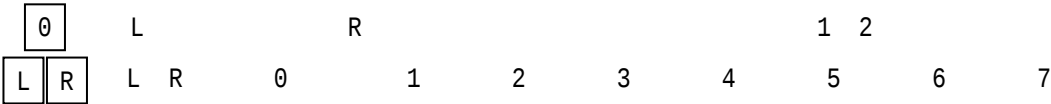
6.22

5.4

JC

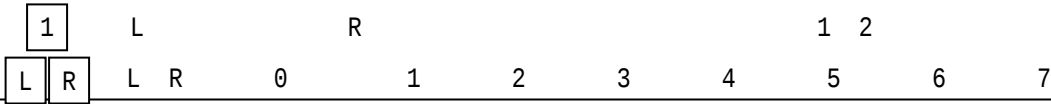
6.1

0 -



6.2

1 -



6.3

2 -

L R

1 2

3 4

5 1 2

6.4 3 -

 3 LR 01=JX 02=INS 01

 L R

6.5

4 -

L R

1 2

30

30

3

4

6.6 5 -

5

LR 01=ZT 02=PARK 03- 

L | R

01

Diagram illustrating a 3D cube with faces labeled L, R, and 6. The top face is 6, the front face is L, and the right face is R. The cube is shown in a perspective view.

[illegible]

6.10 9 -
 9 LR 01=CZ 02=OL 03=OVER LOAD 01
 L R

6.11

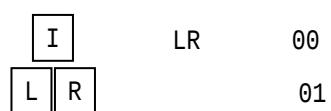
A -

LR 01

02

03

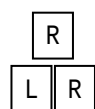
			02		
6.12	B -				
	L		R	1	1
	L R	1			
		2			
		3		1	2
6.13	C -				
	LR	01=XF 02=FR 03=FIRE		01	
6.14	D -				
	LR	01			
		02			
		03			
		04			
		03			
		MCID 48>BDC BT			



00

6.20

R -



L=5 R=5

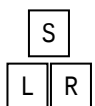
R

L=0 R=0

“ ”

6.21

S -



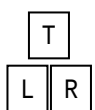
L=5 R=5

S

L=0 R=0

6.22

T -



L=5 R=5

L R

T L=0 R=0

T L=1 R=1